< 0.4 Ω CMOS 1.8 V to 5.5 V, SPST Switches

ADG801/ADG802

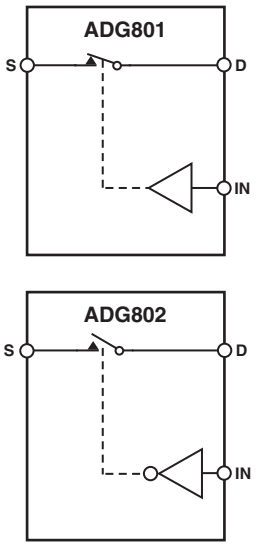
FEATURES

0.4 Ω Max ON Resistance @ 125°C
0.08 Ω Max ON Resistance Flatness @ 125°C
1.8 V to 5.5 V Single Supply
Automotive Temperature Range –40°C to +125°C
400 mA Current Carrying Capability
Tiny 6-Lead SOT-23 and 8-Lead µSOIC Packages
35 ns Switching Times
Low Power Consumption
TTL/CMOS-Compatible Inputs
Pin Compatible with ADG701/ADG702

APPLICATIONS

Power Routing
Cellular Phones
Modems
PCMCIA Cards
Hard Drives
Data Acquisition Systems
Communication Systems
Relay Replacement
Battery-Powered Systems

FUNCTIONAL BLOCK DIAGRAMS



SWITCHES SHOWN FOR A LOGIC "1" INPUT

GENERAL DESCRIPTION

The ADG801/ADG802 are monolithic CMOS, SPST (Single Pole, Single Throw) switches with On Resistance of less than 0.4 Ω. These switches are designed on an advanced submicron process that provides extremely low On Resistance, high switching speed, and low leakage currents.

The low On Resistance of < 0.4 Ω means these parts are ideal for applications where low On Resistance switching is critical.

The ADG801 is a normally open (NO) switch, while the ADG802 is normally closed (NC). Each switch conducts equally well in both directions when On.

The ADG801 and ADG802 are available in 6-lead SOT-23 and 8-lead µSOIC packages.

PRODUCT HIGHLIGHTS

1. Low On Resistance (0.25 Ω typical)
2. 1.8 V to 5.5 V single-supply operation
3. Tiny 6-lead SOT-23 and 8-lead µSOIC packages
4. 400 mA current carrying capability
5. Automotive temperature range –40°C to +125°C
6. Pin-compatible with ADG701 (ADG801)
   Pin-compatible with ADG702 (ADG802)

REV. 0

One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106, U.S.A.
Tel: 781/329-4700 **www.analog.com**
Fax: 781/326-8703

ADG801/ADG802–SPECIFICATIONS[1] (V_{DD} = 5 V ±10%, GND = 0 V. All specifications –40°C to +125°C, unless otherwise noted.)

Parameter	25°C	–40°C to +85°C	–40°C to +125°C[2]	Unit	Test Conditions/Comments
ANALOG SWITCH					
Analog Signal Range			0 V to V_{DD}	V	
On Resistance (R_{ON})	0.25			Ω typ	V_S = 0 V to V_{DD}, I_S = 100 mA;
	0.3	0.35	0.4	Ω max	Test Circuit 1
On Resistance Flatness ($R_{FLAT(ON)}$)	0.05			Ω typ	V_S = 0 V to V_{DD}, I_S = 100 mA
		0.07	0.08	Ω max	
LEAKAGE CURRENTS					V_{DD} = 5.5 V
Source OFF Leakage I_S (OFF)	±0.01			nA typ	V_S = 4.5 V/1 V, V_D = 1 V/4.5 V;
	±0.25	±3	±30	nA max	Test Circuit 2
Drain OFF Leakage I_D (OFF)	±0.01			nA typ	V_S = 4.5 V/1 V, V_D = 1 V/4.5 V;
	±0.25	±3	±30	nA max	Test Circuit 2
Channel ON Leakage I_D, I_S (ON)	±0.01			nA typ	V_S = V_D = 1 V, or 4.5 V;
	±0.25	±3	±30	nA max	Test Circuit 3
DIGITAL INPUTS					
Input High Voltage, V_{INH}			2.0	V min	
Input Low Voltage, V_{INL}			0.8	V max	
Input Current					
I_{INL} or I_{INH}	0.005			µA typ	V_{IN} = V_{INL} or V_{INH}
			±0.1	µA max	
C_{IN}, Digital Input Capacitance	5			pF typ	
DYNAMIC CHARACTERISTICS[3]					
t_{ON}	35			ns typ	R_L = 50 Ω, C_L = 35 pF
	45	50	55	ns max	V_S = 3 V; Test Circuit 4
t_{OFF}	9			ns typ	R_L = 50 Ω, C_L = 35 pF
	15	18	21	ns max	V_S = 3 V; Test Circuit 4
Charge Injection	50			pC typ	V_S = 2.5 V, R_S = 0 Ω; C_L = 1 nF; Test Circuit 5
Off Isolation	–61			dB typ	R_L = 50 Ω, C_L = 5 pF; f = 100 kHz; Test Circuit 6
Bandwidth –3 dB	12			MHz typ	R_L = 50 Ω, C_L = 5 pF; Test Circuit 7
C_S (OFF)	180			pF typ	f = 1 MHz
C_D (OFF)	180			pF typ	f = 1 MHz
C_D, C_S (ON)	420			pF typ	f = 1 MHz
POWER REQUIREMENTS					V_{DD} = 5.5 V
I_{DD}	0.001			µA typ	Digital Inputs = 0 V or 5.5 V
		1.0	2.0	µA max	

NOTES
[1]Temperature range is as follows: Automotive Temperature Range: –40°C to +125°C.
[2]On Resistance parameters tested with I_S = 10 mA.
[3]Guaranteed by design, not subject to production test.

Specifications subject to change without notice.

SPECIFICATIONS[1] (V_{DD} = 2.7 V to 3.6 V, GND = 0 V. All specifications –40°C to +125°C, unless otherwise noted.)

Parameter	25°C	–40°C to +85°C	–40°C to +125°C[2]	Unit	Test Conditions/Comments
ANALOG SWITCH					
Analog Signal Range			0 V to V_{DD}	V	
On Resistance (R_{ON})	0.4			Ω typ	V_S = 0 V to V_{DD}, I_S = 100 mA;
	0.6	0.65	0.7	Ω max	Test Circuit 1
On Resistance Flatness ($R_{FLAT(ON)}$)	0.1	0.1	0.1	Ω typ	V_S = 0 V to V_{DD}, I_S = 100 mA
LEAKAGE CURRENTS					V_{DD} = 3.6 V
Source OFF Leakage I_S (OFF)	±0.01			nA typ	V_S = 3.3 V/1 V, V_D = 1 V/3.3 V;
	±0.25	±3	±30	nA max	Test Circuit 2
Drain OFF Leakage I_D (OFF)	±0.01			nA typ	V_S = 3.3 V/1 V, V_D = 1 V/3.3 V;
	±0.25	±3	±30	nA max	Test Circuit 2
Channel On Leakage I_D, I_S (ON)	±0.01			nA typ	V_S = V_D = 1 V, or 3.3 V;
	±0.25	±3	±30	nA max	Test Circuit 3
DIGITAL INPUTS					
Input High Voltage, V_{INH}			2.0	V min	
Input Low Voltage, V_{INL}			0.8	V max	
Input Current					
I_{INL} or I_{INH}	0.005			μA typ	V_{IN} = V_{INL} or V_{INH}
			±0.1	μA max	
C_{IN}, Digital Input Capacitance	5			pF typ	
DYNAMIC CHARACTERISTICS[3]					
t_{ON}	40			ns typ	R_L = 50 Ω, C_L = 35 pF
	55	60	65	ns max	V_S = 1.5 V; Test Circuit 4
t_{OFF}	9			ns typ	R_L = 50 Ω, C_L = 35 pF
	15	18	21	ns max	V_S = 1.5 V; Test Circuit 4
Charge Injection	10			pC typ	V_S = 1.5 V, R_S = 0 Ω, C_L = 1 nF; Test Circuit 5
Off Isolation	–61			dB typ	R_L = 50 Ω, C_L = 5 pF, f = 100 kHz, Test Circuit 6
Bandwidth –3 dB	12			MHz typ	R_L = 50 Ω, C_L = 5 pF; Test Circuit 7
C_S (OFF)	180			pF typ	f = 1 MHz
C_D (OFF)	180			pF typ	f = 1 MHz
C_D, C_S (ON)	420			pF typ	f = 1 MHz
POWER REQUIREMENTS					V_{DD} = 3.6 V
I_{DD}	0.001			μA typ	Digital Inputs = 0 V or 3.6 V
		1.0	2.0	μA max	

NOTES

[1]Temperature range is as follows: Automotive Temperature Range: –40°C to +125°C.

[2]On Resistance parameters tested with I_S = 10 mA.

[3]Guaranteed by design, not subject to production test.

Specifications subject to change without notice.

ABSOLUTE MAXIMUM RATINGS[1]

(T_A = 25°C unless otherwise noted.)

V_{DD} to GND . . . . . –0.3 V to +7 V
Analog Inputs[2] . . . . . –0.3 V to V_{DD} +0.3 V
or 30 mA, Whichever Occurs First
Digital Inputs[2] . . . . . –0.3 V to V_{DD} +0.3 V
or 30 mA, Whichever Occurs First
Continuous Current, S or D . . . . . 400 mA
Peak Current, S or D . . . . . 800 mA
(Pulsed at 1 ms, 10% Duty Cycle Max)
Operating Temperature Range Automotive . –40°C to +125°C
Storage Temperature Range . . . . . –65°C to +150°C
Junction Temperature ($T_J$max) . . . . . 150°C
Package Power Dissipation . . . . . ($T_J$max – T_A)/θ_{JA}
µSOIC Package
θ_{JA} Thermal Impedance . . . . . 206°C/W
θ_{JC} Thermal Impedance . . . . . 44°C/W
SOT-23 Package (4-Layer Board)
θ_{JA} Thermal Impedance . . . . . 119°C/W
Lead Temperature, Soldering (10 seconds) . . . . . 300°C
IR Reflow, Peak Temperature (<20 seconds) . . . . . 235°C

NOTES

[1]Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those listed in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Only one absolute maximum rating may be applied at any one time.

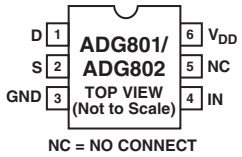
[2]Overvoltages at IN, S, or D will be clamped by internal diodes. Current should be limited to the maximum ratings given.

Table I. Truth Table

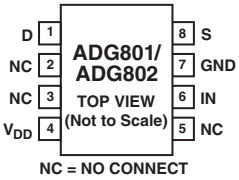
ADG801 In	ADG802 In	Switch Condition
0	1	OFF
1	0	ON

PIN CONFIGURATIONS

6-Lead Plastic Surface-Mount (SOT-23)
(RT-6)



8-Lead Small Outline µSOIC
(RM-8)



ORDERING GUIDE

Model	Temperature Range	Brand[1]	Package Descriptions	Package Options
ADG801BRT	–40°C to +125°C	SLB	SOT-23 (Plastic Surface-Mount)	RT-6[2]
ADG801BRM	–40°C to +125°C	SLB	µSOIC (Small Outline)	RM-8
ADG802BRT	–40°C to +125°C	SMB	SOT-23 (Plastic Surface-Mount)	RT-6[2]
ADG802BRM	–40°C to +125°C	SMB	µSOIC (Small Outline)	RM-8

[1]Branding on SOT-23 and µSOIC packages is limited to three characters due to space constraints.
[2]Contact factory for availability.

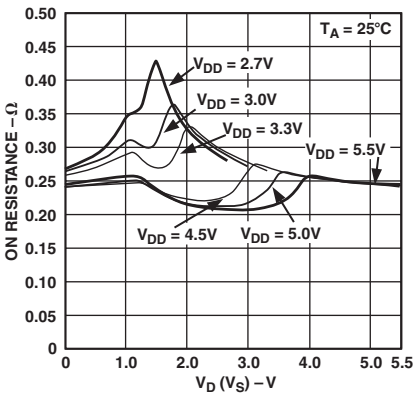
CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the ADG801/ADG802 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high-energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

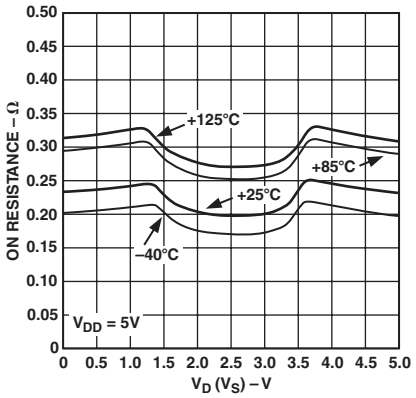
TERMINOLOGY

V_{DD}	Most Positive Power Supply Potential
I_{DD}	Positive Supply Current
GND	Ground (0 V) Reference
S	Source Terminal. May be an input or output.
D	Drain Terminal. May be an input or output.
IN	Logic Control Input
V_D (V_S)	Analog Voltage on Terminals D and S
R_{ON}	Ohmic Resistance Between D and S
$R_{FLAT(ON)}$	The difference between the maximum and minimum value of On Resistance as measured over the specified analog signal range.
I_S (OFF)	Source Leakage Current with the Switch OFF
I_D (OFF)	Drain Leakage Current with the Switch OFF
I_D, I_S (ON)	Channel Leakage Current with the Switch ON
V_{INL}	Maximum Input Voltage for Logic "0"
V_{INH}	Minimum Input Voltage for Logic "1"
I_{INL} (I_{INH})	Input Current of the Digital Input
C_S (OFF)	OFF Switch Source Capacitance. Measured with reference to ground.
C_D (OFF)	OFF Switch Drain Capacitance. Measured with reference to ground.
C_D, C_S (ON)	ON Switch Capacitance. Measured with reference to ground.
C_{IN}	Digital Input Capacitance
t_{ON}	Delay between applying the Digital Control Input and the Output Switching ON. See Test Circuit 4.
t_{OFF}	Delay between applying the Digital Control Input and the Output Switching OFF.
Charge Injection	A measure of the glitch impulse transferred from the Digital Input to the Analog Output during switching.
Off Isolation	A measure of unwanted signal coupling through an OFF switch.
Bandwidth	The frequency at which the output is attenuated by 3 dBs.
On Response	The frequency response of the ON switch
Insertion Loss	The loss due to the On Resistance of the switch

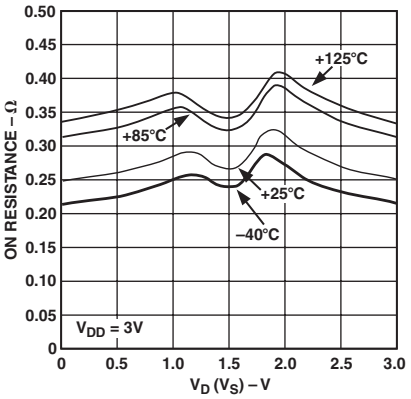
ADG801/ADG802–Typical Performance Characteristics



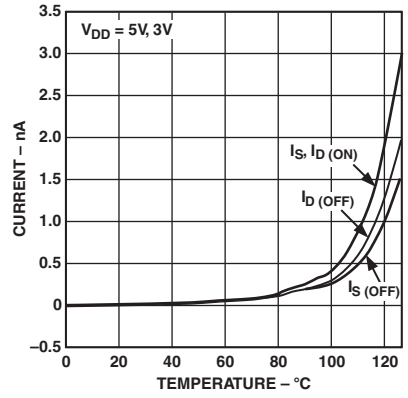
TPC 1. On Resistance vs. V_D (V_S)



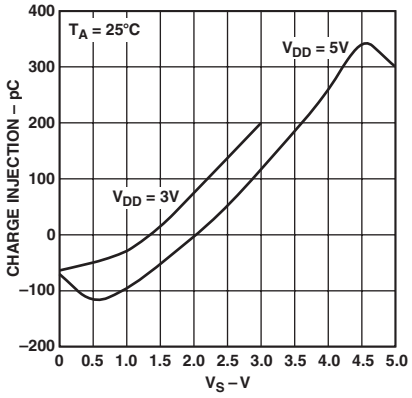
TPC 2. On Resistance vs. V_D (V_S) for Different Temperatures



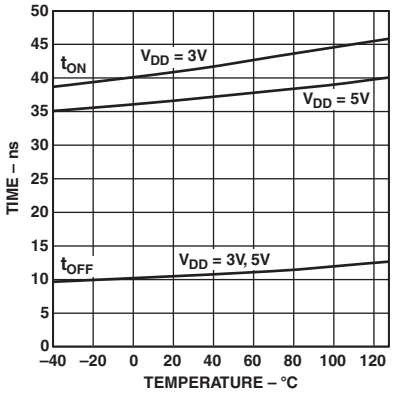
TPC 3. On Resistance vs. V_D (V_S) for Different Temperatures



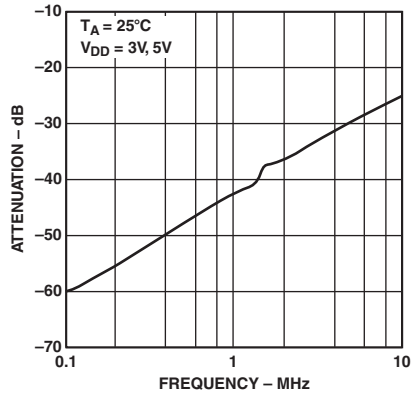
TPC 4. Leakage Currents vs. Temperature



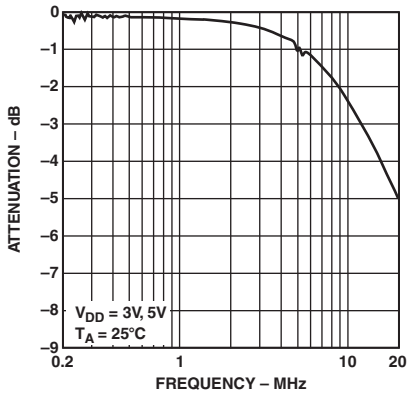
TPC 5. Charge Injection vs. Source Voltage



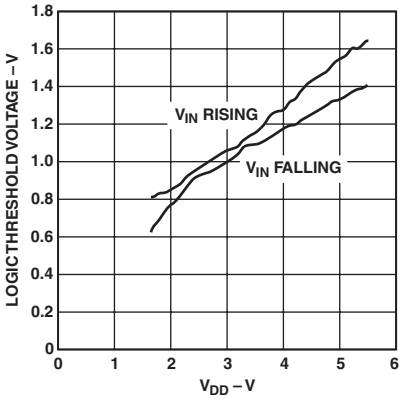
TPC 6. t_{ON}/t_{OFF} Times vs. Temperature



TPC 7. Off Isolation vs. Frequency

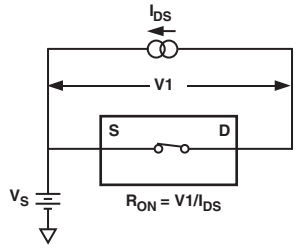


TPC 8. On Response vs. Frequency

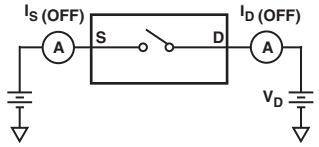


TPC 9. Logic Threshold Voltage vs. Supply Voltage

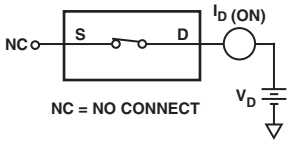
Test Circuits



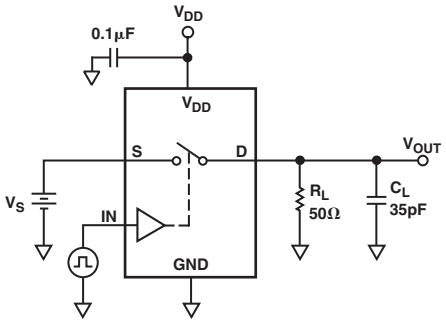
Test Circuit 1. On Resistance

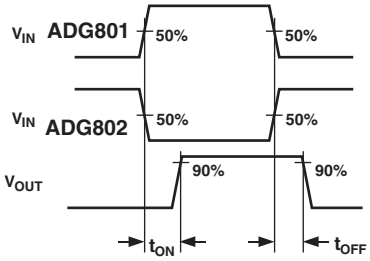


Test Circuit 2. Off Leakage

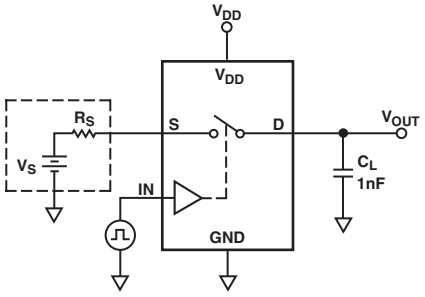


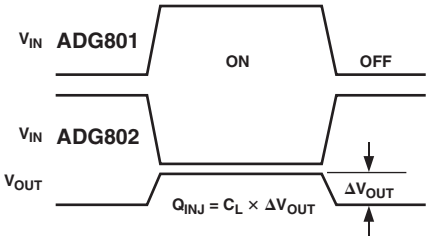
Test Circuit 3. On Leakage



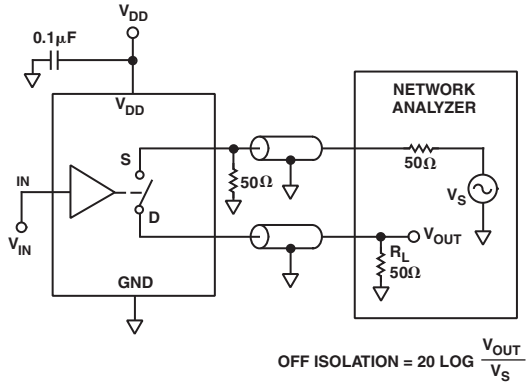


Test Circuit 4. Switching Times

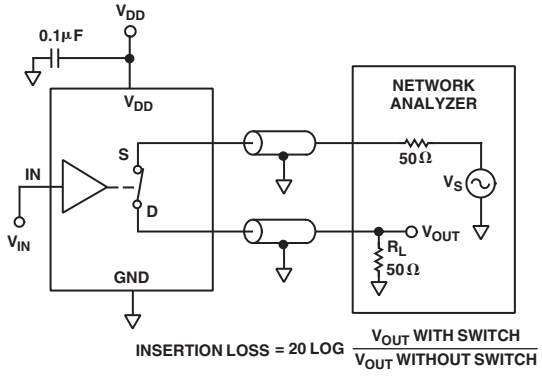




Test Circuit 5. Charge Injection



Test Circuit 6. Off Isolation

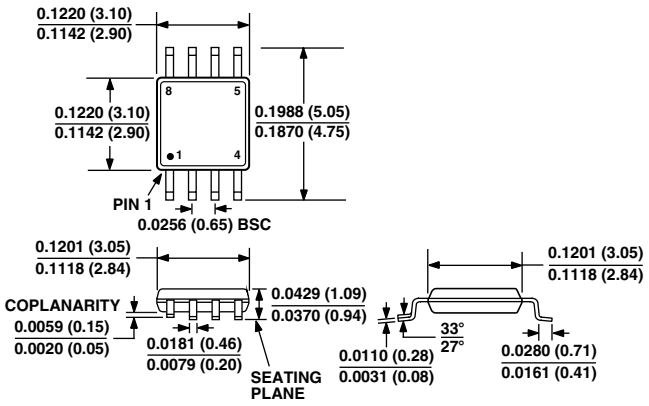


Test Circuit 7. Bandwidth

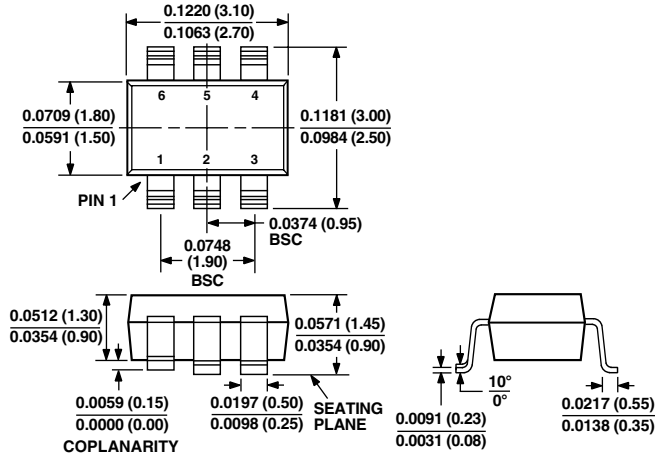
OUTLINE DIMENSIONS

Dimensions shown in inches and (mm)

8-Lead μSOIC (RM-8)



6-Lead SOT-23 (RT-6)



C02800–0–5/02(0)

PRINTED IN U.S.A.